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buffer for buffering bitstream of the signal

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## Patents

Patents **1 - 10** on **buffer for buffering bitstream of the signal**. (0.07 seconds)

## ▸ List view

[Cover view](#)[Rate control strategies for speech and music coding](#)

US Pat. 6647366 - Filed Dec 28, 2001 - Microsoft Corporation

The method of claim 1, wherein the step of determining 20 a **signal** type ... bit-stream **buffer** exceeds a predefined pre-**buffering** number of encoding frames; ...

## ▸ Sort by relevance

[Sort by date \(new first\)](#)[Sort by date \(old first\)](#)[Rational input \*\*buffer\*\* arrangements for auxiliary information in video and ...](#)

US Pat. 5481543 - Filed Jun 10, 1994 - Sony Corporation

Then, the information **signal** is encoded using the encoder with the size of the ... 6 A shows ideal **buffering** in the video input **buffer** in the decoder of the

...

## ▸ Any status

[Issued patents](#)[Applications](#)[Data block format for software carrier and player therefor](#)

US Pat. 5463565 - Filed Oct 29, 1993 - Time Warner Entertainment Co., L.P.

A system in accordance with claim 14 wherein said **buffer** means are long ... A method in accordance with claim 19 wherein said **buffering** capacities are large

...

[Adaptive rate control for insertion of data into arbitrary bit rate data streams](#)

US Pat. 6219358 - Filed Sep 11, 1998 - Scientific-Atlanta, Inc.

... **buffer for buffering** said outgoing MPEG bit stream; and a packet handler ... providing a **signal**; equipment for encoding the **signal** into a bit stream; ...[System and method for frame accurate splicing of compressed bitstreams](#)

US Pat. 6611624 - Filed Oct 15, 1998 - Cisco Systems, Inc.

Otherwise, the j5 spliced **bitstream** is the first **bitstream** before the .... of data buffered by the first pre-**buffer** 602 and this **signal** is referred to as ...[Method and apparatus of providing bit count integrity and synchronous data ...](#)

US Pat. 5844885 - Filed Jun 11, 1996 - Qualcomm Incorporated

Obviously **buffer** prefill X area 154 introduces a unavoidable fixed delay. ... A sync reset occurs when **buffering** queue runs out of data so that it cannot ...[Method and an apparatus for interleaving \*\*bitstream\*\* to record thereof on a ...](#)

US Pat. 5784528 - Filed Sep 27, 1996 - Matsushita Electric Industrial Co. Ltd.

20 However, in the case that the **signal** processor 2008 of the **bitstream** reproducer 2000 is incorporated with a **buffer** for error correction process, ...[Method and an optical disc for generating a \*\*bitstream\*\* containing a plurality ...](#)

US Pat. 5884004 - Filed Sep 27, 1996 - Matsushita Electric Industrial Co., Ltd.

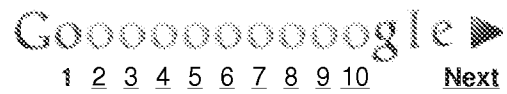
However, in the case that the **signal** processor 2008 of the **bitstream** reproducer 2000 is incorporated with a **buffer** for error correction process, ...[Method and an apparatus for reproducing \*\*bitstream\*\* having non-sequential ...](#)

US Pat. 5923869 - Filed Sep 27, 1996 - Matsushita Electric Industrial Co., Ltd.

However, in the case that the **signal** processor 2008 of the **bitstream** reproducer 2000 is incorporated with a **buffer** for error correction process, ...[Intelligent digital \*\*signal\*\* hitless protection switch](#)

US Pat. 5577196 - Filed Nov 30, 1995 - Sprint Communications Co. L.P.

The **signal** from CSU 605 45 enters both differential delay controller 645 and delay **buffer** 610. Similarly, the digital **signal** stream having check bit ...



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